

1 Inventor: Pai-Hung Pan

2 Title: Semiconductor Processing Methods of Forming a Conductive

3 Gate and Line

4 Assignee: Micron Technology, Inc.



#4/IDS

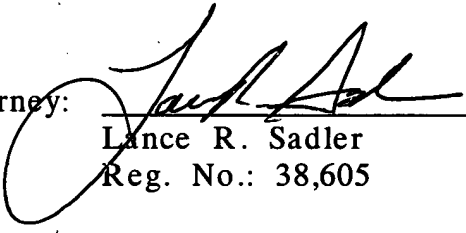
6 INFORMATION DISCLOSURE STATEMENT

7 References -- See Attached Form PTO-1449

8 The attached form PTO-1449 is submitted in compliance  
9 with 37 CFR §1.56.

10 Respectfully submitted,

11  
12 Dated: 4/13/98

13 Attorney: 

Lance R. Sadler  
Reg. No.: 38,605